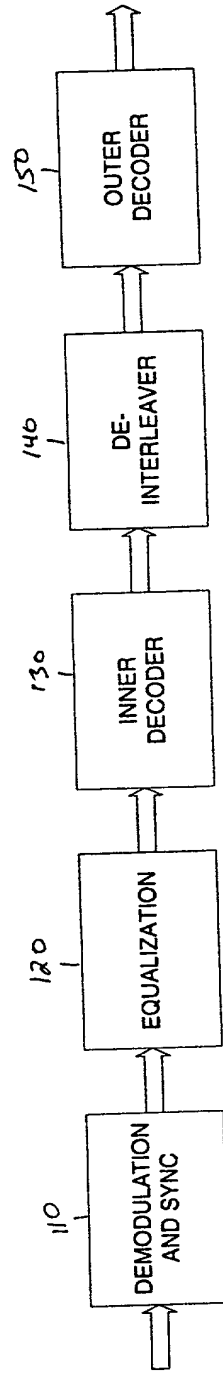


100

FIGURE 1



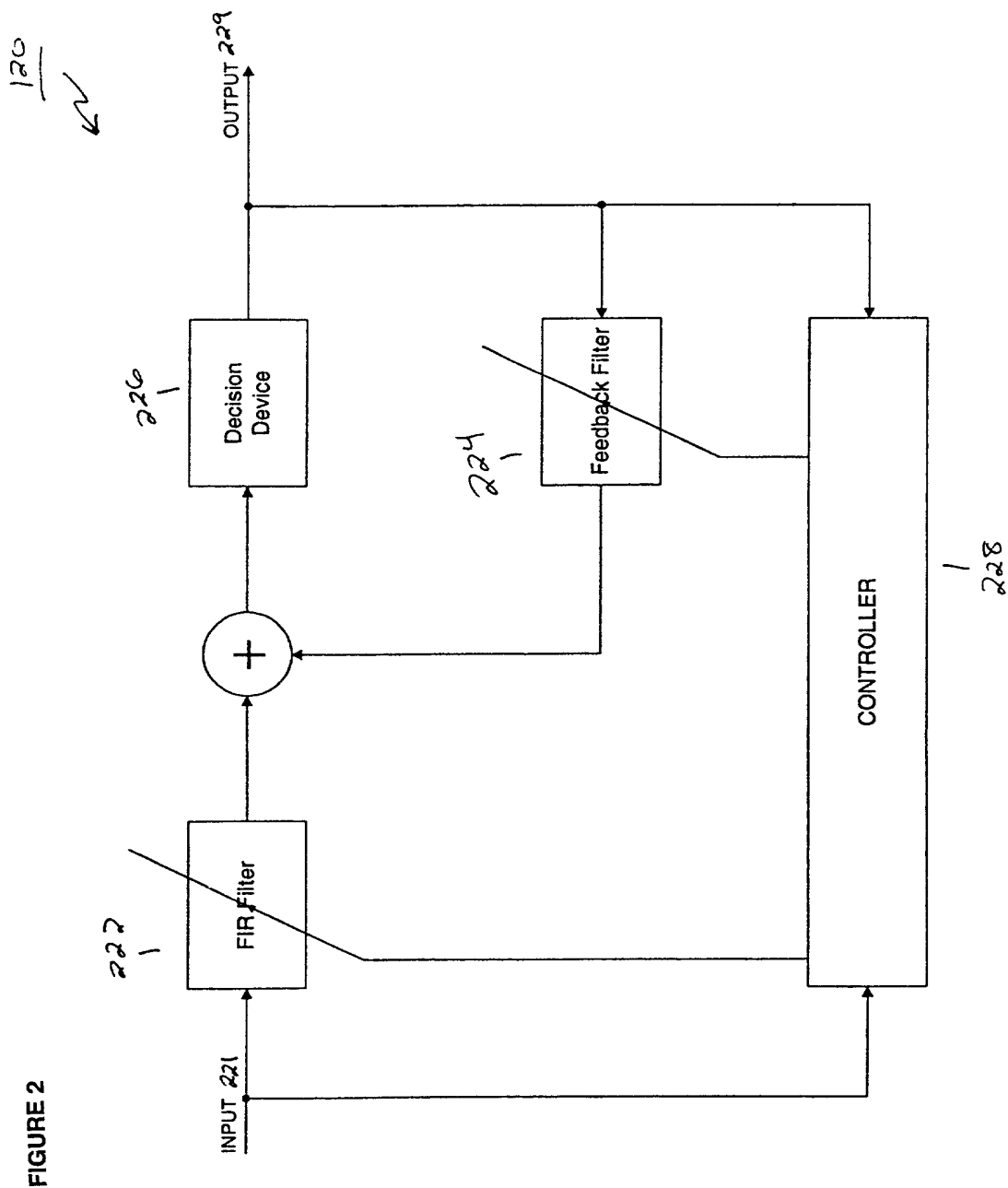


FIGURE 4

400
2

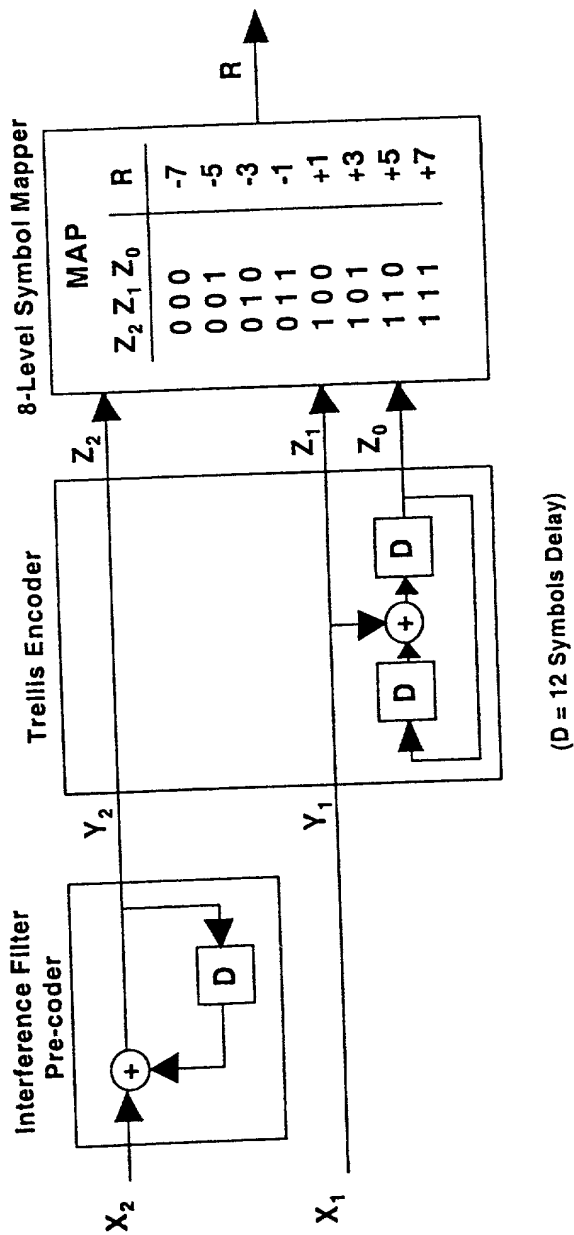


FIGURE 6

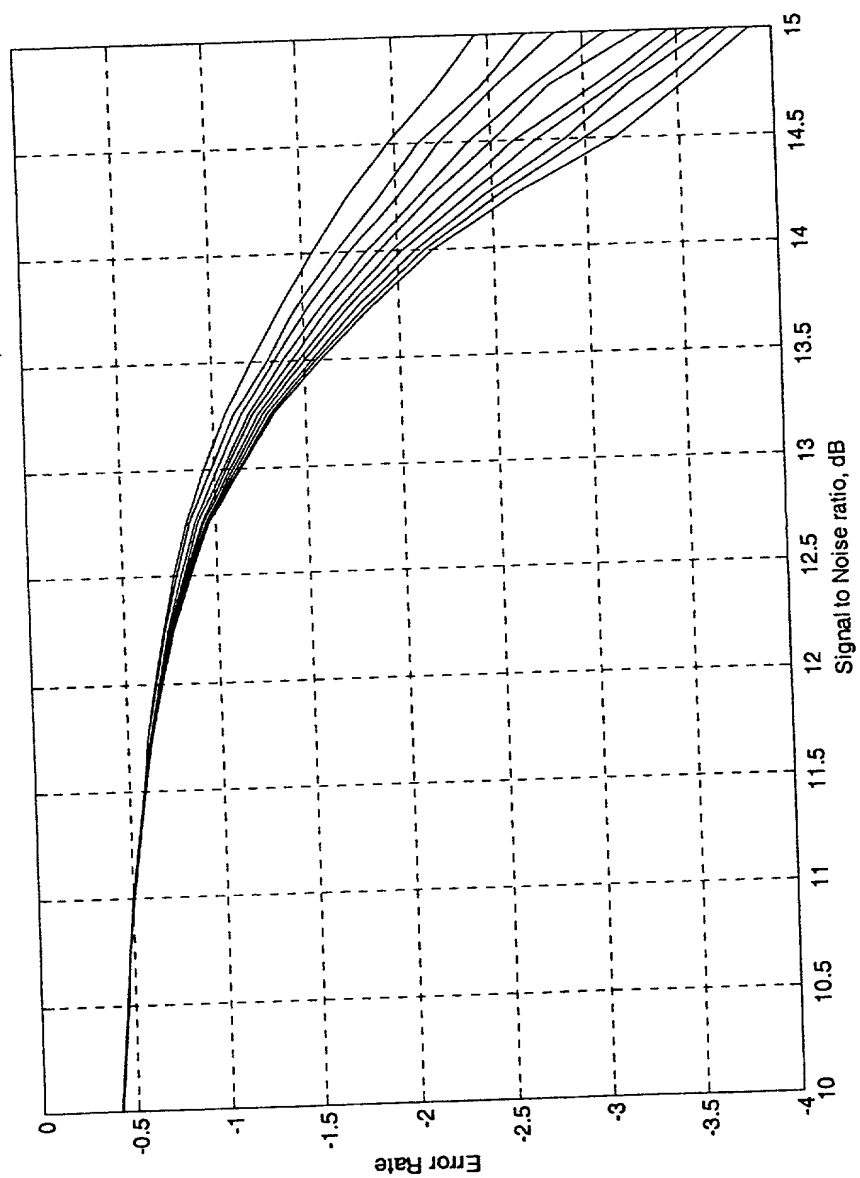


FIGURE 7

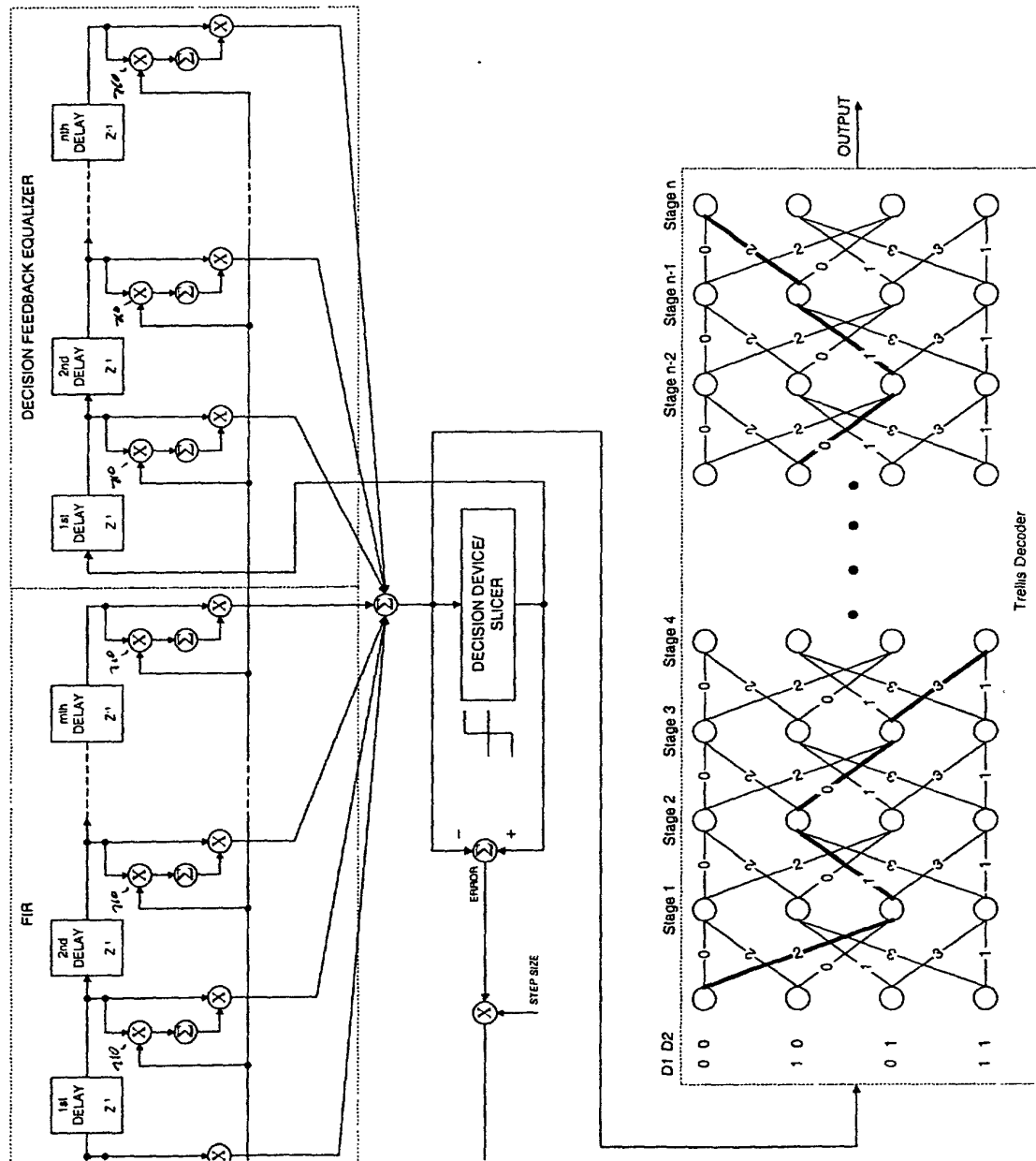


FIG. 8 is a block diagram of a multi-stage trellis decoder system.

FIGURE 8

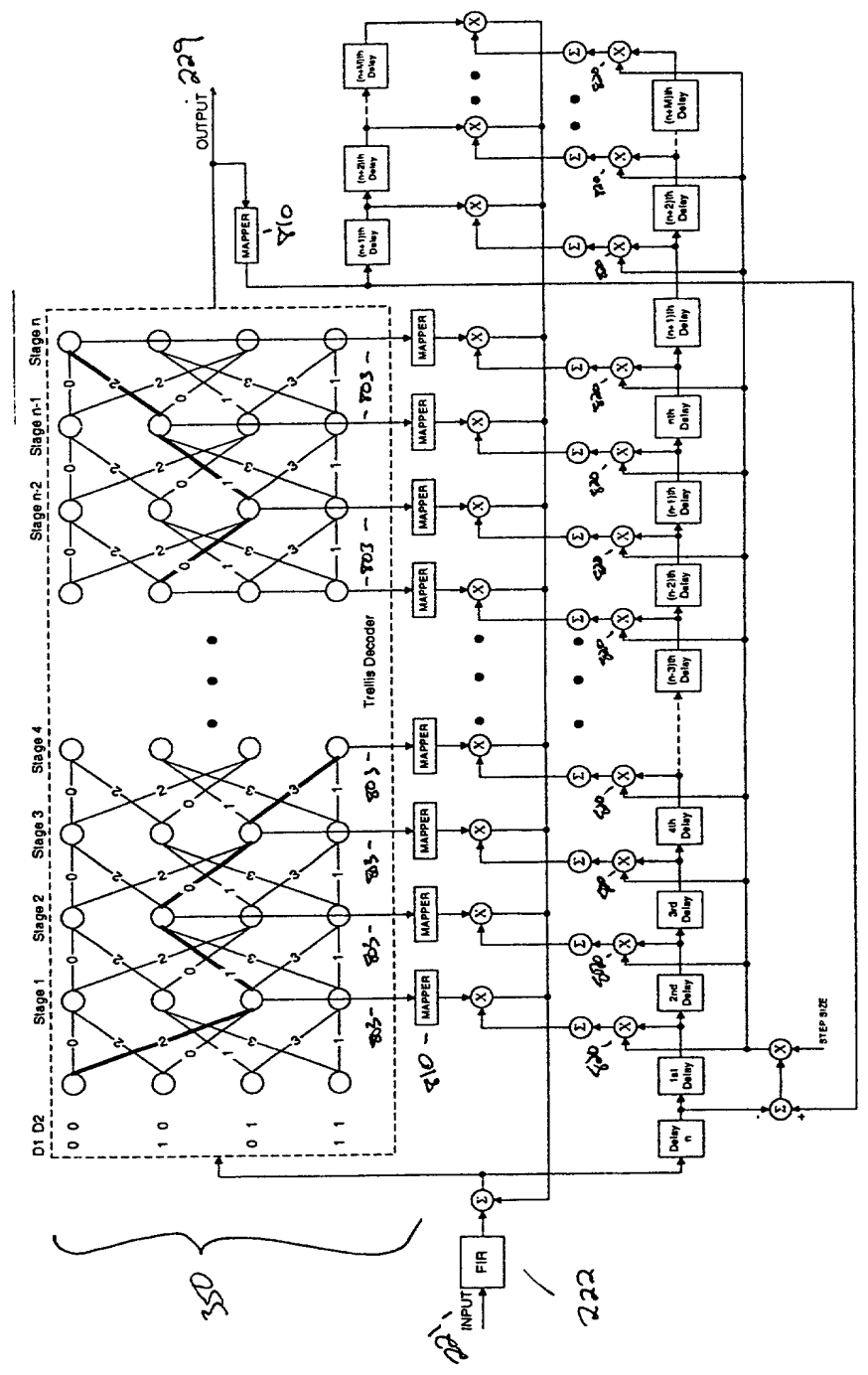


FIGURE 9

